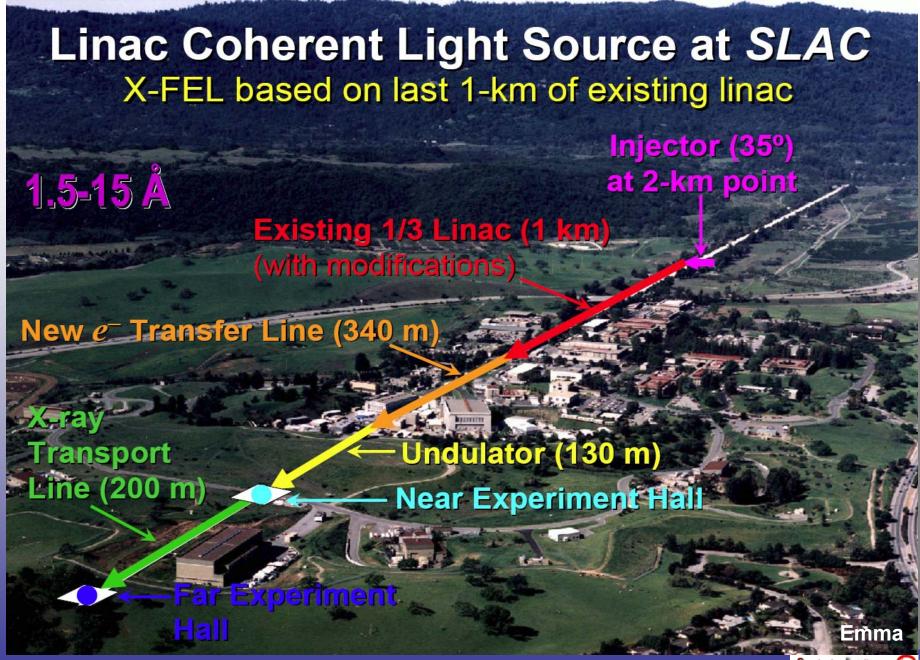
Linac Coherent Light Source (LCLS)

Low Level RF Status

2007 LLRF Workshop Knoxville, TN

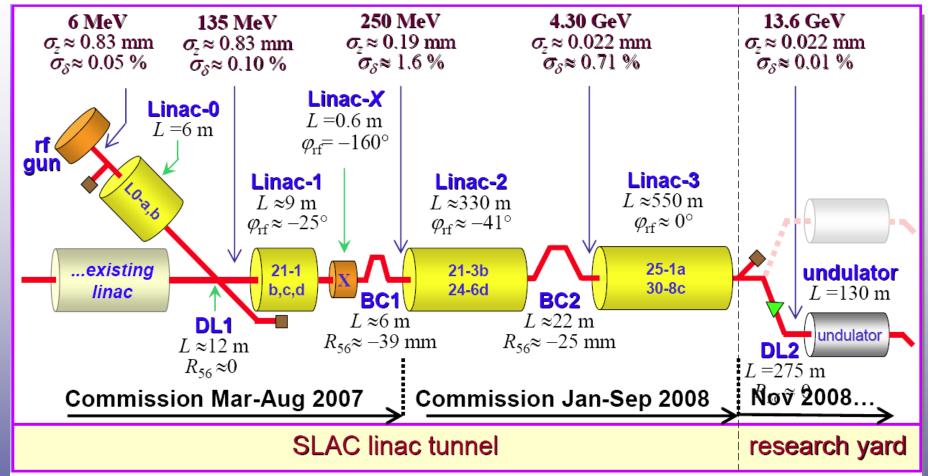


October 22, 2007

Ron Akre, Dayle Kotturi

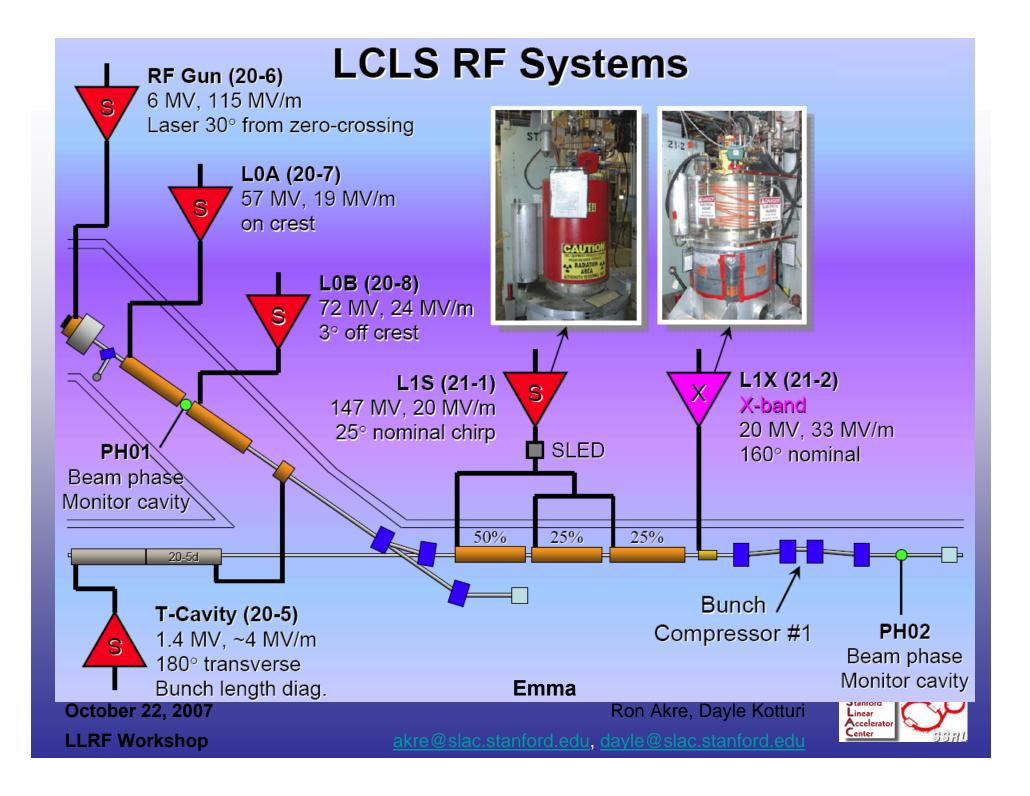
Stanford Linear Accelerator Center



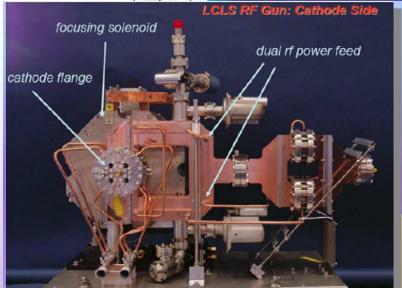


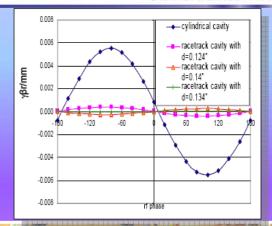
X-rays in spring 2009

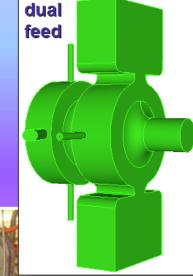
Emma



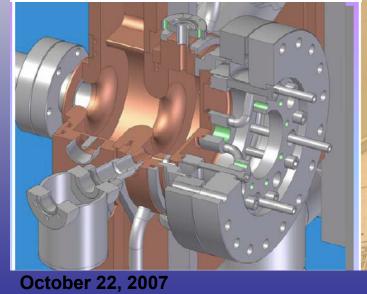








SLAC Designed RF Gun



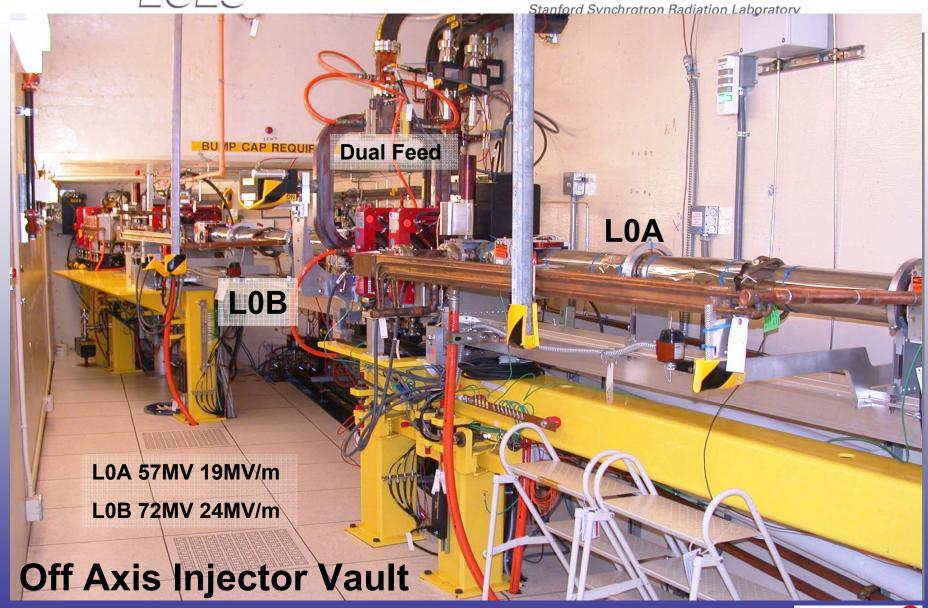


LLRF Workshop









October 22, 2007

LLRF Workshop

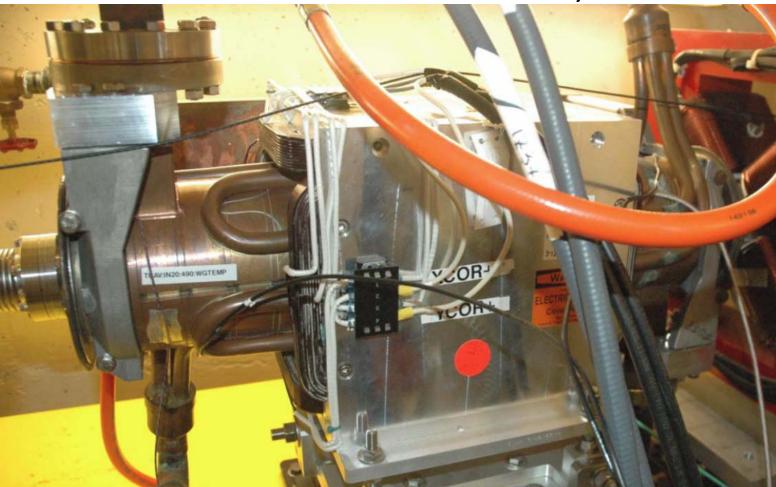






Injector Transverse Accelerator 55cm 1MV

Powered from 20-5 Linac Klystron Accelerator Output. The klystron station is up stream of LCLS. The transverse accelerator is located in the off axis injector.

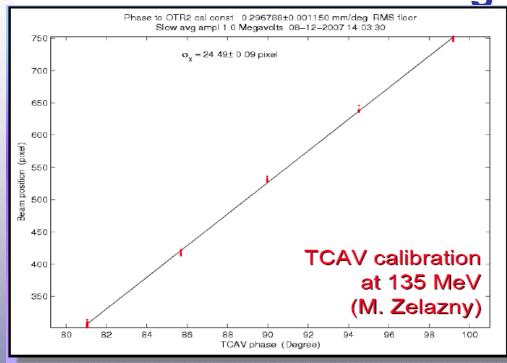


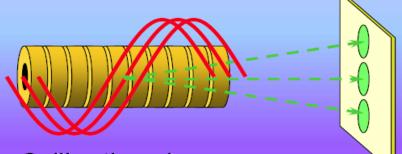
October 22, 2007





Calibration to Degrees of S-Band



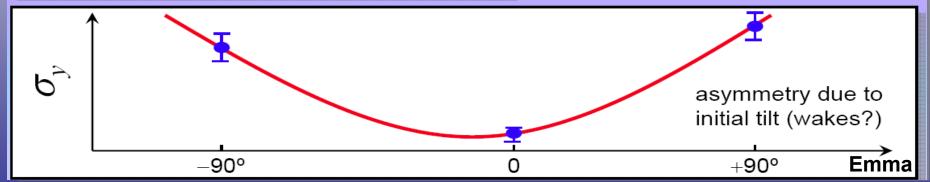


Calibration gives screen pixels per deg-S (P. Krejcik)

Now measure beam size at:

- 1. positive zero-crossing,
- 2. negative zero-crossing, &
- 3. with RF off

Fit beam-size data to parabola...



October 22, 2007

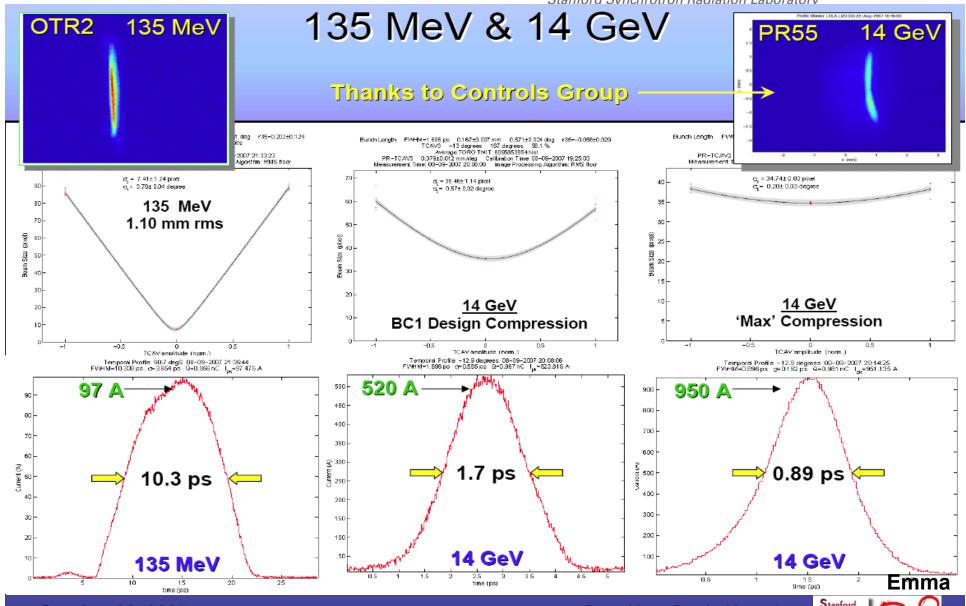
LLRF Workshop





Bunch Length Measurements Stanford Linear Accelerator Center

Stanford Synchrotron Radiation Laboratory

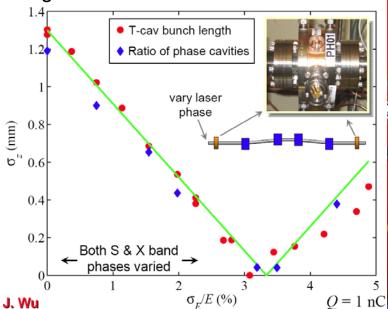


October 22, 2007 **LLRF Workshop**





Located between LOA and LOB. Single cell cavity at 2805MHz. 51MHz below RF frequency to lower from dark current generated in the RF Gun. 2805MHz is 25.5MHz below Local Oscillator to enable beam phase measurement against LO reference. Measurement below correlates differences in beam phase between cavities before and after BC1 to bunch length.



Beam Phase Cavity



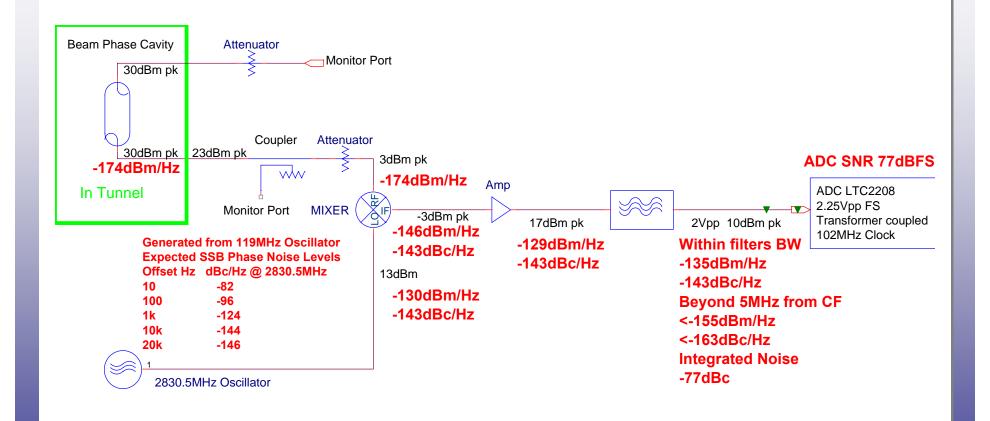
October 22, 2007

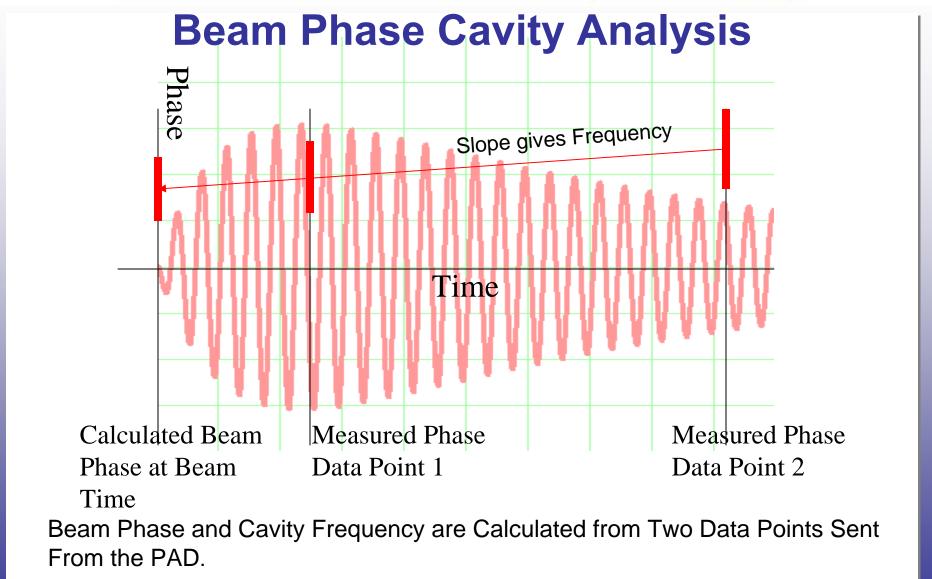
LLRF Workshop





Beam Phase Cavity Critical Noise Levels and Bandwidths







X-Band Structure in Main Linac

20MV - 33MV/m

15MW at structure

22MW at the klystron

120nS fill time.

Beam at +160degrees

20° from decelerating crest

THE PROPERTY OF THE PARTY OF TH



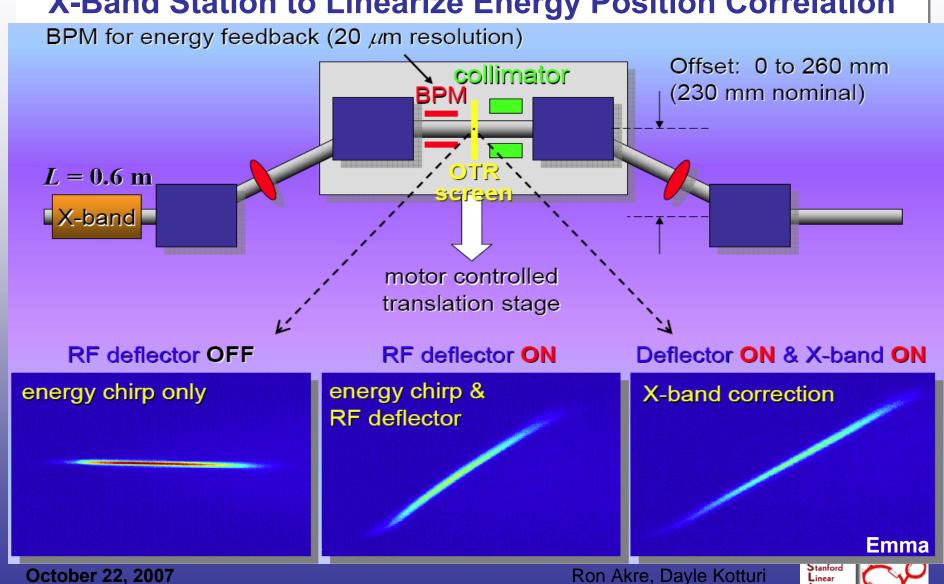
October 22, 2007

LLRF Workshop





X-Band Station to Linearize Energy Position Correlation



LLRF Workshop







LCLS RF Jitter Tolerance Budget

 $|\langle \Delta E/E_0 \rangle| < 0.1\%$ and $|\Delta I/I_0| < 12\%$

Parameter	Symbol	LCLS	Unit
Gun timing jitter	Δt_0	0.50	psec
Initial bunch charge	$\Delta Q/Q_0$	2.0	%
mean L0 rf phase	φ_0	0.10	deg
mean L1 rf phase	φ_1	0.10	deg
mean L h rf phase X-band	$arphi_h$	0.50	X-deg
mean L2 rf phase	φ_2	0.07	deg
mean L3 rf phase	φ_3	0.15	deg
mean L0 rf voltage	$\Delta V_0/V_0$	0.10	%
mean L1 rf voltage	$\Delta V_1/V_1$	0.10	%
mean Lh rf voltage	$\Delta V_h/V_h$	0.25	%
mean L2 rf voltage	$\Delta V_2/V_2$	0.10	%
mean L3 rf voltage	$\Delta V_3/V_3$	0.08	%

Lowest Noise Floor Requirement

0.5deg X-Band = 125fS Structure Fill time = 100nS Noise floor = -111dBc/Hz @ 11GHz 10MHz BW -134dBc/Hz @ 476MHz

RMS tolerance budget for <12% rms peak-current jitter or <0.1% rms final e- energy jitter. All tolerances are rms levels and the voltage and phase tolerances per klystron for L2 and L3 are √Nk larger, assuming uncorrelated errors, where Nk is the number of klystrons per linac.

P. Emma



Slow Drift Tolerance Limits

(Top 4 rows for $\Delta \varepsilon < 5\%$, bottom 4 limited by feedback dynamic range)

Gun-Laser Timing	±2.4*	deg-S
Bunch Charge	±3.2	%
Gun RF Phase	±2.3	deg-S
Gun Relative Voltage	±0.6	%
L0,1,X,2,3 RF Phase (approx.)	±5	deg-S
L0,1,X,2,3 RF Voltage (approx.)	±5	%

(Tolerances are peak values, not rms)

P. Emma, J. Wu

^{*} for synchronization, this tolerance might be set to ± 1 ps (without arrival-time measurement)





Linac Sector 0 RF Upgrade LCLS must be compatible with the existing linac operation including PEP timing shifts MAIN LINAC (SECTOR 0) RF/TIMING SYSTEM **Master Oscillator is** located 1.3 miles W Sum from LCLS Injector **Fiducial MASTER** 1.3 Miles to 476MHz MASTER to RF **AMPLIFIERS** 476MHz **OSCILLATOR LCLS** Injector Measurements on PEP PHASE SHIFTER +-720 Degrees in 0.5mS January 20, 2006 SLC COUNTDOWN **IQPAU** at Sector 21 CHASSIS 476MHz PEP Control Divide to 8.5MHz show 30fS rms jitter Camac Module 8.5MHz in a bandwidth from 10Hz to 10MHz Master Trigger Fiducial Generator Generator MTG Syncronized to: 360Hz 360Hz Line 360Hz Power Line Syncs Fiducial to Sync. 8.5MHz Damping Ring 8.5MHz Damping Ring and 360Hz Power Line 476MHz RF Distribution 8.5MHz Damping Ring Frequency +20 FIDUCIAL 360Hz PULSE 476MHz RF -20 MDL 476MHz RF WITH PEP PHASE SHIFT ON MAIN DRIVE LINE MDL RF with TIMING Pulse – Sync to DR

October 22, 2007

Ron Akre, Dayle Kotturi akre@slac.stanford.edu, dayle@slac.stanford.edu

Stanford Linear Accelerator Center

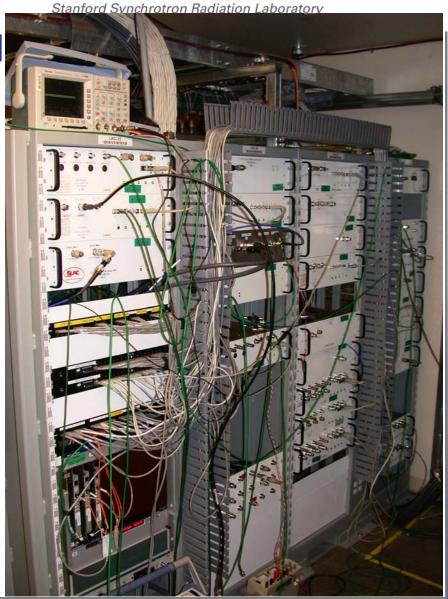


LCLS LLRF System to BC1

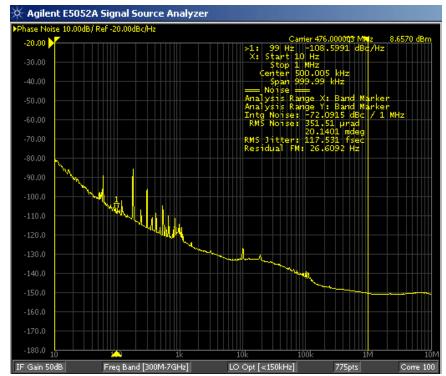
- RF Feedback for Six RF Stations
 - Gun 5 Chassis
 - L0A 4 Chassis
 - L0B 4 Chassis
 - TCav 4 Chassis
 - L1S 4 Chassis
 - L1X 4 Chassis
- Laser Reference and Feedback 2
- Two Phase Cavities 1 Chassis
- Reference System 21 Chassis

Total of 49 SLAC built RF chassis were installed and turned on last run.

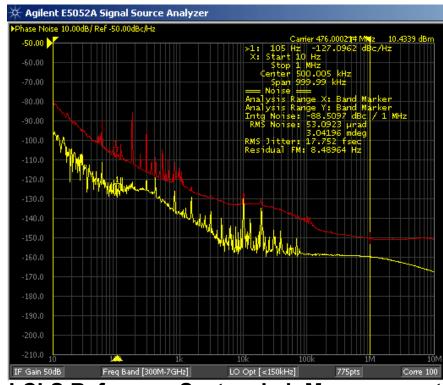
Four short racks in a temperature controlled RF Hut contain the RF reference system as well as Phase and Amplitude Detectors (PADs) for critical RF measurements.



RF Distribution Lab vs. MDL Measurements

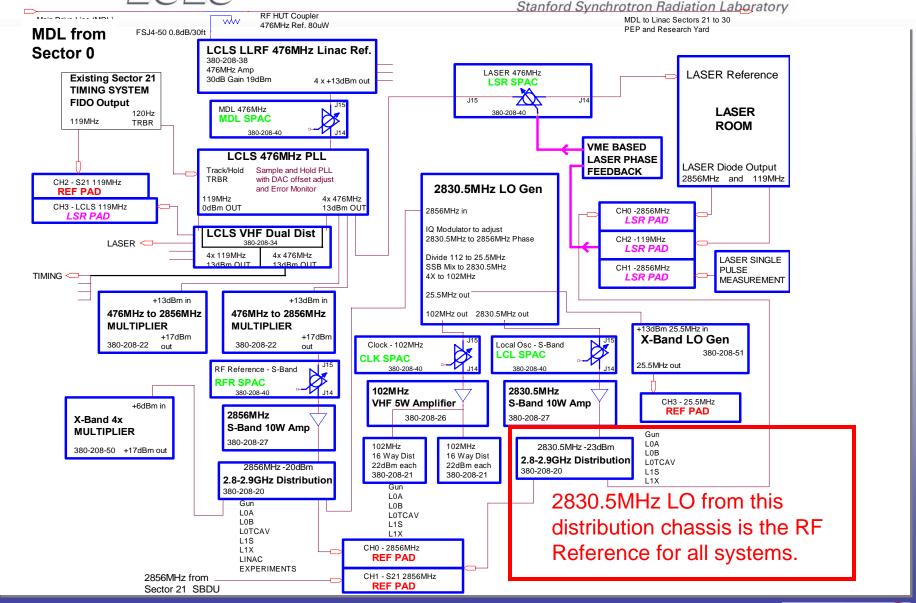


Existing Linac MDL Sector 0
126fS rms Jitter 10Hz to 10MHz



LCLS Reference System Lab Measurements
20fS rms Jitter 10Hz to 10MHz

John Byrd LBNL



October 22, 2007





LCLS New Reference System Lab Measurements

Lab Tests Show Reference System Noise Levels Meet All LCLS Requirements

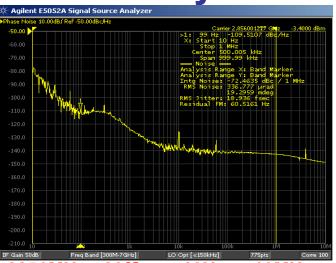
2856MHz = 70fSrms

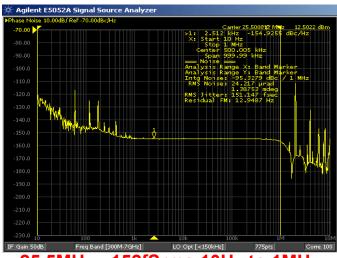
2830.5MHz = 70fSrms

25.5MHz = 2pSrms

102MHz = 2pSrms

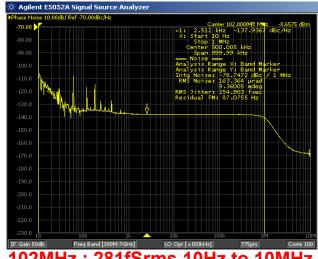
John Byrd - LBNL



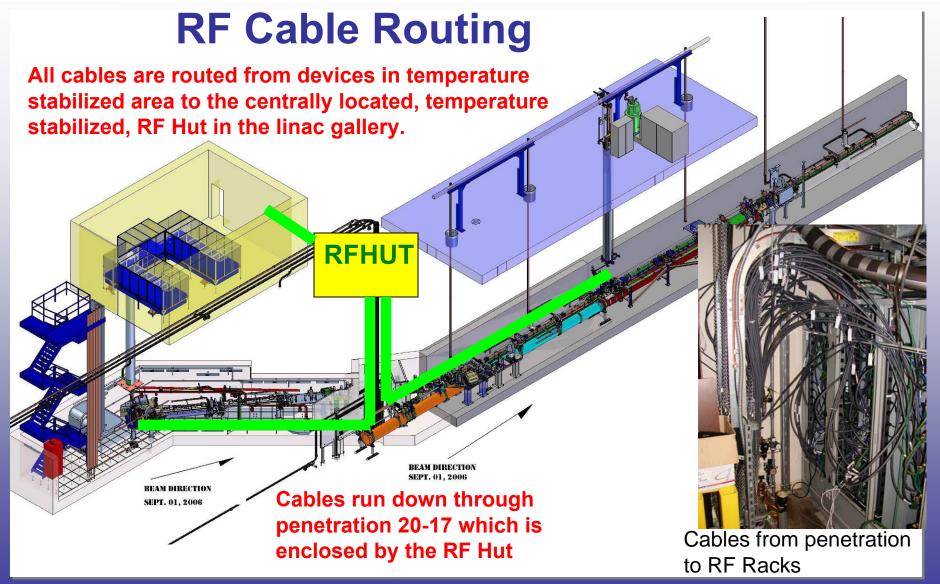




2830.5MHz: 22fSrms 10Hz to 10MHz



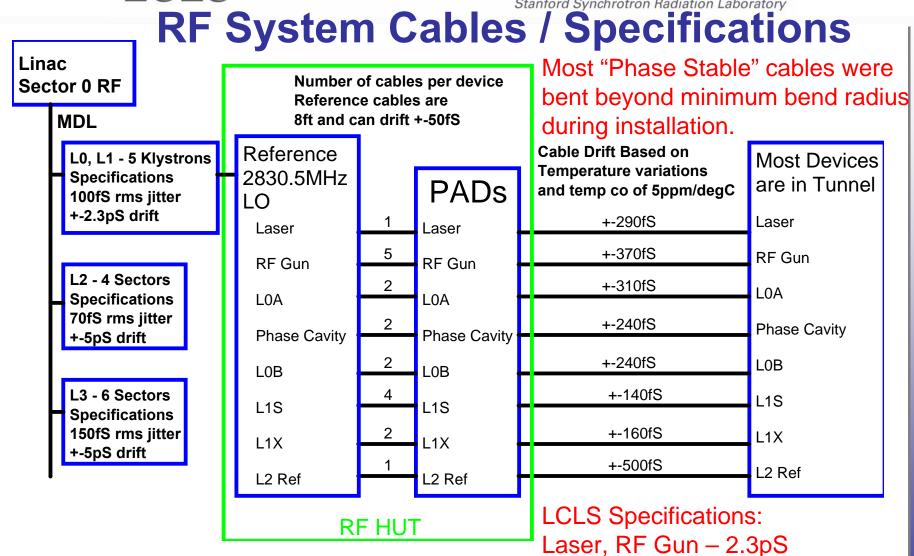




October 22, 2007







October 22, 2007

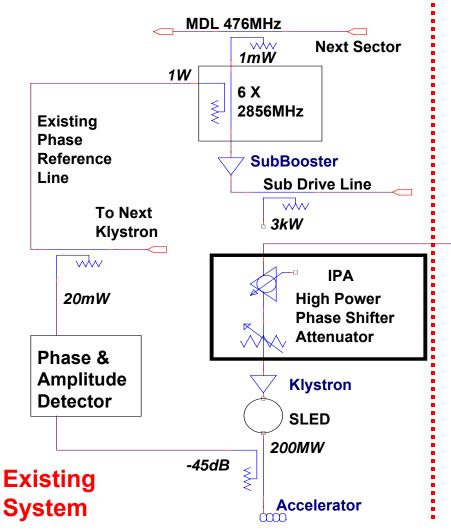
Ron Akre, Dayle Kotturi akre@slac.stanford.edu, dayle@slac.stanford.edu



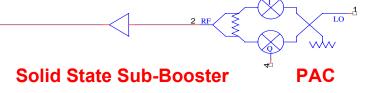
LOA, LOB, L1S, L1X, L2, L3 - 5pS



SLAC Linac RF. – New Control



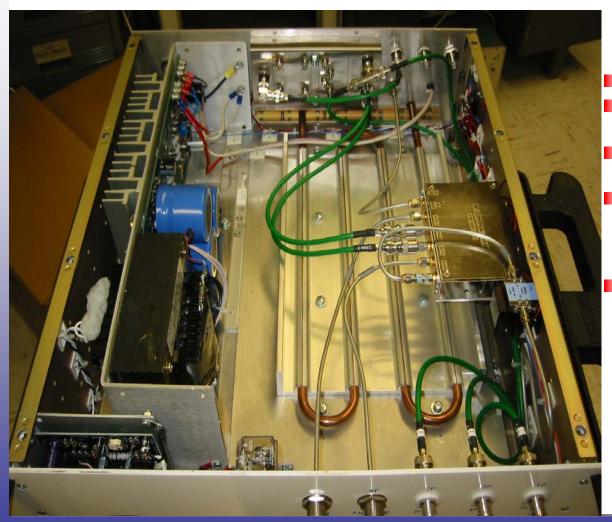
The new control system will tie in to the IPA Chassis with 800W of drive power available. The RF Reference will be from the new RF reference system.



I and Q will be controlled by the PAC chassis, running 16bit DACs at 102MHz. Waveforms to the DACs will be set in an FPGA through a microcontroller running EPICS on RTEMS.



1 kW Solid State S-Band Amplifiers



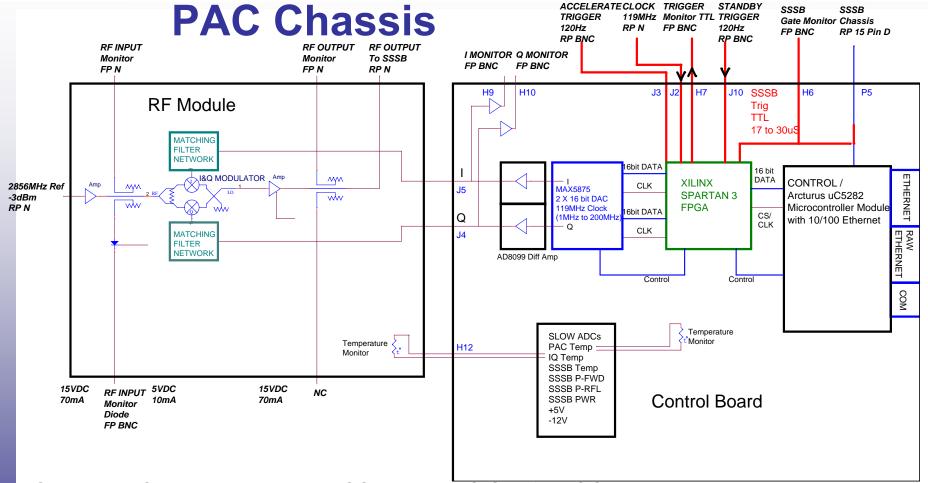
- >800W peak at 2856MHz
- 5 units installed and operational last run
- Added phase noise not measurable
- Trigger comes from Beam Containment System (BCS) Need to change to 48V pulse on Twin BNC connector.
- Amplifier module from Microwave Amplifiers Ltd.

October 22, 2007

LLRF Workshop





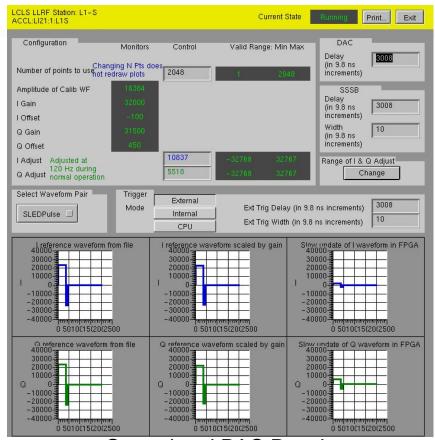


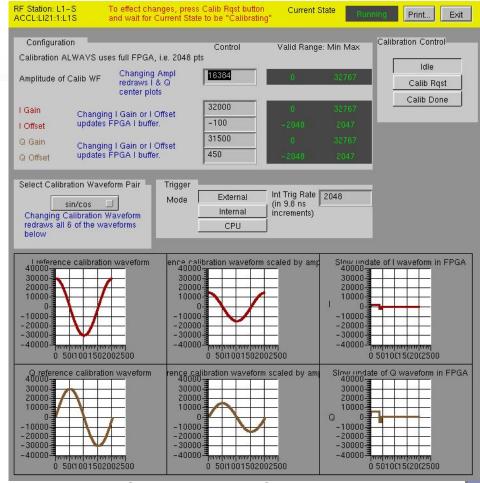
S-Band PAC chassis has an EPICS on RTEMS Coldfire IOC used to load registers and waveform memory on an FPGA. On a trigger the FPGA puts out two 2048 point waveforms which run I and Q inputs on an RF modulator. In calibration mode a single side band modulator is created by sine and cosine waveforms on the I and Q channels.

October 22, 2007



PAC IOC EPICS Panels





Operational PAC Panel

Calibration PAC Panel

In operation mode the PAC receives PVs "I Adjust" and "Q Adjust" which are used to transform a preloaded waveform and then load the FPGA. A future upgrade will have the FPGA transform the I and Q waveforms with the loading of 4 matrix elements. In calibration mode the I and Q Offsets are determined to minimize feedthrough in the RF modulator with the gains set to zero. The modulator gains are then set to maximum and then adjusted down to suppress the opposite sideband in a Single Side Band modulator.

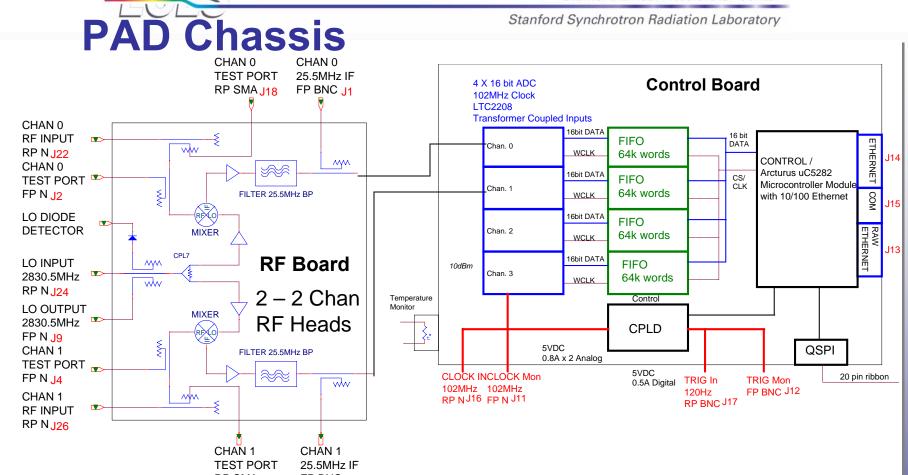
October 22, 2007

Ron Akre, Dayle Kotturi



Linear

Center



S-Band PAC chassis has an EPICS on RTEMS Coldfire IOC which reads 4 FIFOs from the 16 bit 102MHz ADCs. The 4 channel control board is connected to two RF heads, each of which has 2 channels. The RF is down mixed with the 2830.5MHz LO reference to 25.5MHz IF, which is digitized at 102MHz. The IOC does the down conversion to base band, averages over a specified number of points, up to 512, and the set the EPICS I and Q records.

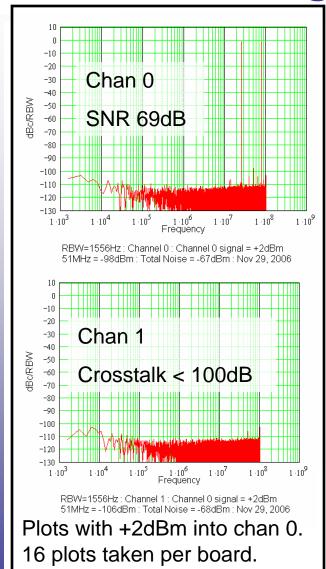
October 22, 2007

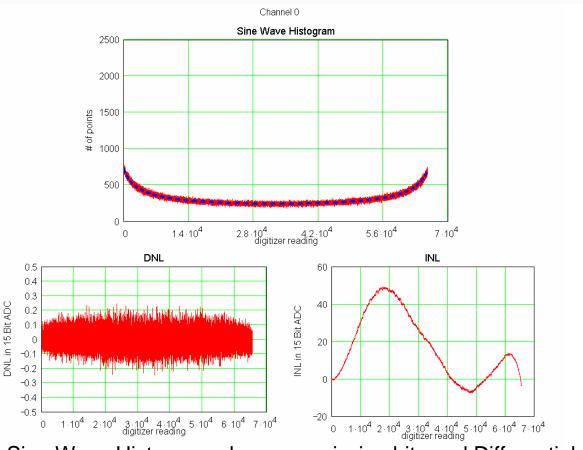
Stanford Linear Accelerator Center akre@slac.stanford.edu, dayle@slac.stanford.edu

PAD Testing

Stanford Linear Accelerator Center

Stanford Synchrotron Radiation Laboratory





Sine Wave Histogram shows no missing bits and Differential Nonlinearity of ±0.2 LSBs. The Integral Nonlinearity is large due to nonlinearities in the function generator used. The lower SNR of 69dB is due mainly to the 4:1 impedance transformers used on both clock and signal inputs.

October 22, 2007 LLRF Workshop

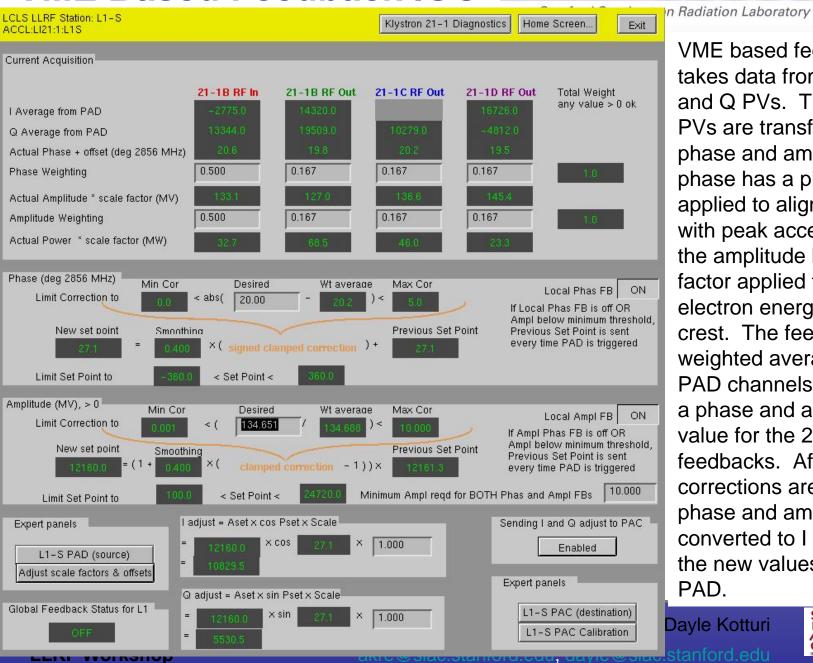


PAD IOC EPICS Panel



The Coldfire EPICS IOC reads digitized data from 4 FIFOs. A window is set in the data by selecting an offset and window size. The data within the window is down converted to baseband and an average I and Q calculated. The data shown here is from station L1S, a SLED cavity is used to power 3 accelarator structures. Channel 0 is the Input to the B structure, channels 1, 2, and 3 are outputs to the B, C, and D structures.

The temperature monitors are shown here not working, we have more work to do.

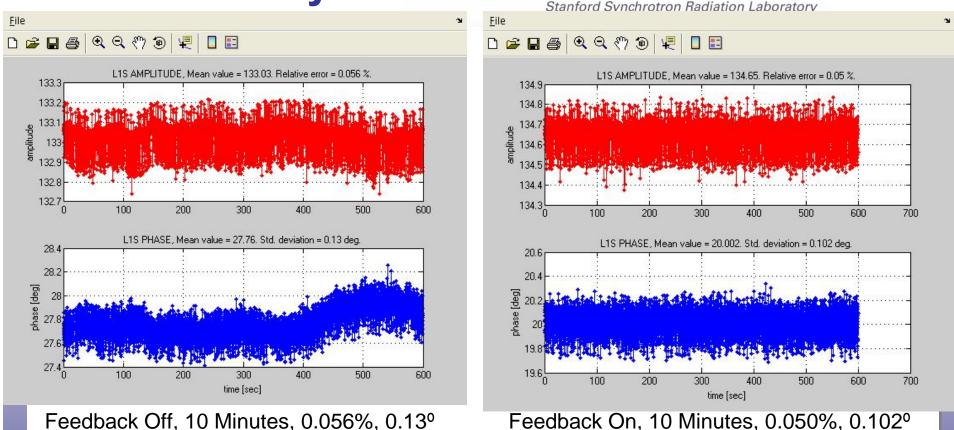


VME based feedback IOC takes data from the PAD I and Q PVs. The I and Q PVs are transformed to phase and amplitude. The phase has a phase offset applied to align 0 phase with peak acceleration and the amplitude has a scale factor applied to read in electron energy gain on crest. The feedback used a weighted average of the 4 PAD channels to determine a phase and amplitude value for the 2 separate feedbacks. After feedback corrections are done the phase and amplitude are converted to I and Q and the new values sent to the PAD.

Stanford

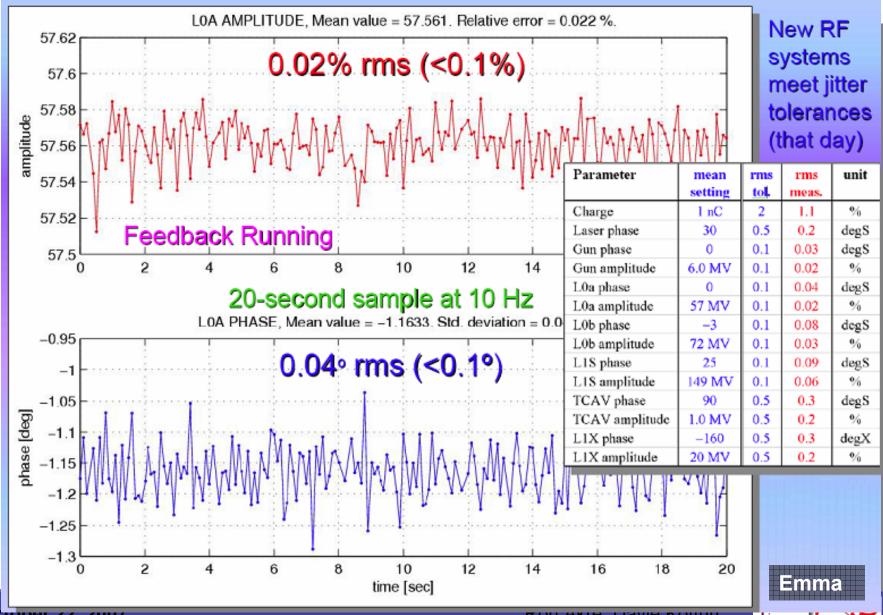
Accelerator Center

Linear



L1S Meets Jitter Specifications (0.1% 0.1°) for 10 minutes with feedback on. All stations except X-Band met specifications consistently near the end of the run. The above data was taken with Matlab routines reading the EPICS records from the VME based feedback.

RF Phase and Amplitude Stability (L0a)



October 22, 2001

KUIT AKIE, Dayle KULLUII

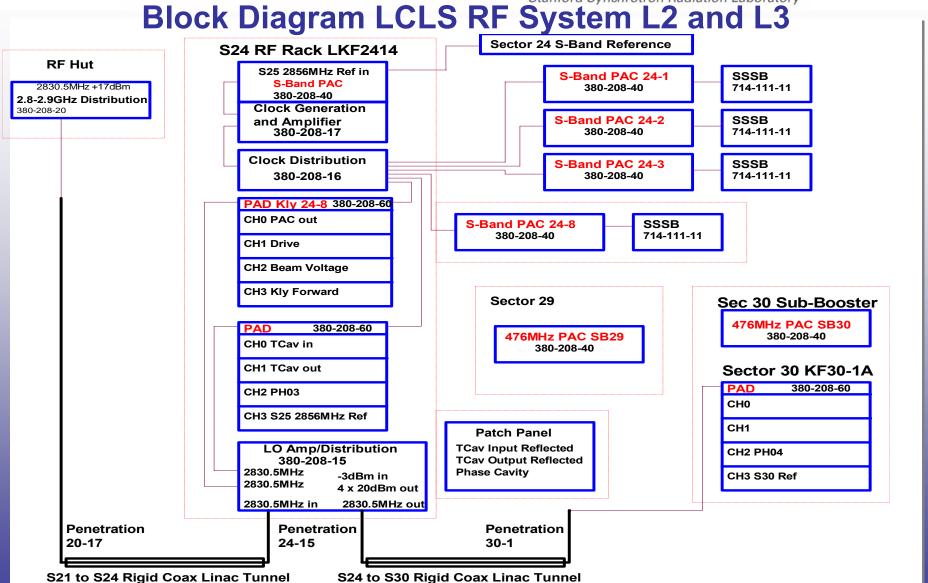
Accelerator Center



LCLS RF System L2 and L3

- Turn On December 2007 4 weeks
- Control of 3 RF stations for L2
- Two Sector Controls (16 RF stations) for L3
- Transverse Cavity Control
- Phase Reference Line in Tunnel (8 Sectors)
- Two Beam Phase Cavities
- Total of 18 SLAC built RF Chassis
- Are we ready? Definitely not, but we will turn on anyway.
- Next we need to finish the injector RF system



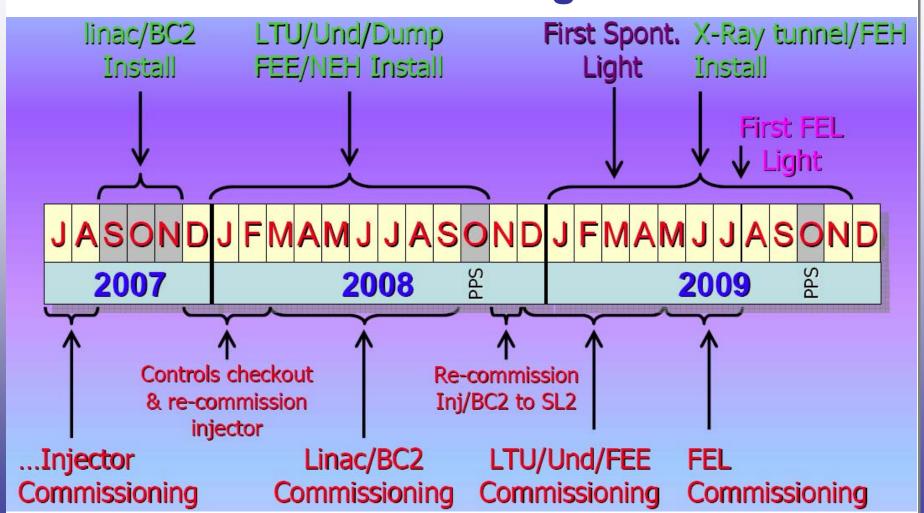


October 22, 2007





LCLS Commissioning Time-Line



October 22, 2007

Ron Akre, Dayle Kotturi

Stanford Linear Accelerator Center



