# The RF Line **UHF Power Transistor**

 $\dots$  designed primarily for wideband, large–signal output and driver amplifier stages to 1000 MHz.

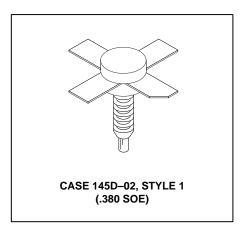
- Designed for Class A Linear Power Amplifiers
- Specified 19 Volt, 1000 MHz Characteristics: Output Power — 7.0 Watts Power Gain — 9.0 dB Min, Small–Signal
- Built-In Matching Network for Broadband Operation
- · Gold Metallization for Improved Reliability
- · Diffused Ballast Resistors
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	28	Vdc
Collector–Base Voltage	VCBO	50	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	3.5	Vdc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	42 0.25	Watts W/°C
Operating Junction Temperature	TJ	200	°C
Storage Temperature Range	T <sub>sta</sub>	-65 to +150	°C

## MRA1000-7L

9.0 dB, TO 1000 MHz 7.0 WATTS BROADBAND UHF POWER TRANSISTOR



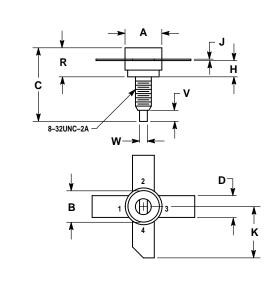
#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (T <sub>C</sub> = 70°C)	$R_{\theta JC}$	4.0	°C/W

#### **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 20 mA, I <sub>B</sub> = 0)	V <sub>(BR)</sub> CEO	28	_	_	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 20 mA, V <sub>BE</sub> = 0)	V(BR)CES	50	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 20 mA, I <sub>E</sub> = 0)	V(BR)CBO	50	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 5.0 mA, I <sub>C</sub> = 0)	V(BR)EBO	3.5	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 19 V, I <sub>E</sub> = 0)	ICBO	1	_	15	mAdc
ON CHARACTERISTICS					
DC Current Gain (I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 5.0 V)	hFE	20	_	90	_
DYNAMIC CHARACTERISTICS					
Output Capacitance (V <sub>CB</sub> = 24 V, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	_	22	pF
FUNCTIONAL TESTS					
Common–Emitter Amplifier Small–Signal Gain (V <sub>CE</sub> = 19 V, f = 1.0 GHz, I <sub>C</sub> = 1.2 A)	G <sub>SS</sub>	9.0	10	_	dB
Load Mismatch (VCE = 19 V, IC = 1.2 A, P <sub>out</sub> = 7.0 W, f = 1.0 GHz, Load VSWR = ∞:1, All Phase Angles)	Ψ	No Degradation in Output Power			
Overdrive (V <sub>CE</sub> = 19 V, I <sub>C</sub> = 1.2 A, f = 1.0 GHz) (No degradation)	Pinover	_	_	3.5	W
Output Power, 1.0 dB Compression Point (V <sub>CE</sub> = 19 V, f = 1.0 GHz, I <sub>C</sub> = 1.2 A)	P <sub>o1 dB</sub>	7.0	_	_	W

#### PACKAGE DIMENSIONS



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
  Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.320	0.385	9.28	9.77
В	0.320	0.330	8.13	8.38
С	0.700	0.778	17.78	19.76
D	0.220	0.230	5.59	5.84
Н	0.160	0.170	4.07	4.31
J	0.003	0.006	0.08	0.15
K	0.490	0.520	12.45	13.20
R	0.248	0.275	6.30	7.23
V	0.100	0.130	2.54	3.30
W	0.055	0.065	1.40	1.65

STYLE 1:

- PIN 1. EMITTER 2. BASE
  - 3 FMITTER
  - 4 COLLECTOR

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