### Advance Information

# The RF Small Signal Line

# **Gallium Arsenide PHEMT**

## **Pseudomorphic High Electron Mobility Transistor**

Designed for use in low voltage, moderate power amplifiers such as portable analog and digital cellular radios and PC RF modems.

- Performance Specifications at 3.5 V, 850 MHz: Output Power = 31 dBm Min Power Gain = 11 dB Typ Efficiency = 70% Min
- Guaranteed Ruggedness at Load VSWR = 20:1
- New Plastic Surface Mount Package
- Available in Tape and Reel Packaging Options:
   T1 suffix = 1,000 Units per Reel
- Device Marking = 9822

### MRF9822T1

31 dBm, 850 MHz HIGH FREQUENCY POWER TRANSISTOR GaAs PHEMT



CASE 449-02, STYLE 1 (PLD-1)

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain-Gate Voltage	$V_{DGO}$	12	Vdc
Gate-Source Voltage	V <sub>GS</sub>	- 6	Vdc
Drain Current – Continuous	ΙD	3	Adc
Total Device Dissipation @ T <sub>C</sub> = 50°C Derate above 50°C	PD	10 100	W mW/°C
Storage Temperature Range	T <sub>stg</sub>	- 65 to +150	°C
Operating Temperature Range	TJ	150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	10	°C/W

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

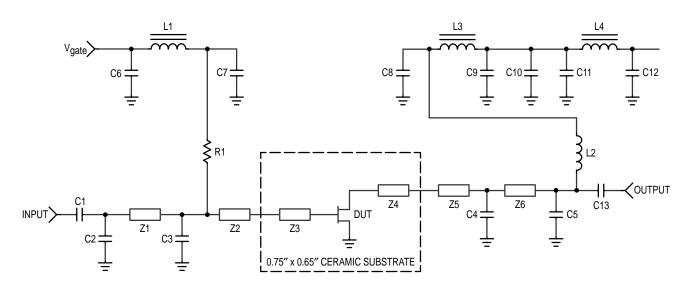
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Drain-Gate Breakdown Voltage (I <sub>D</sub> = 1.5 mA)	BV <sub>GDO</sub>	12	ı	ı	Vdc
Off–state Leakage Current (VDS = 5.5 V, VGS = -2. 6 V)	I <sub>DS(off)</sub>	-	-	3	mA
Gate–Source Leakage Current (V <sub>GS</sub> = -2. 6 V)	lgss	-	-	10	μAdc

NOTE – <u>CAUTION</u> – MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.



### **ELECTRICAL CHARACTERISTICS – continued** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
Gate Threshold Voltage (V <sub>DS</sub> = 3.5 V, I <sub>D</sub> = 150 mA)	VGS(th)	-1.5	-	-0.5	Vdc
Forward Transconductance (VDS = 6 V, ID = 200 mA)	9fs	-	1.5	-	mhos
Saturation Drain-Current (VGS = 0.0 V, VDS = 1.5 V)	IDSS	1.8	2.5	-	А
FUNCTIONAL CHARACTERISTICS					
Power Gain (V <sub>DD</sub> = 3.5 Vdc, P <sub>in</sub> = 20 dBm, I <sub>DQ</sub> = 150 mA, f = 850 MHz)	G <sub>ps</sub>	10.5	11	-	dB
Drain Efficiency (V <sub>DD</sub> = 3.5 Vdc, P <sub>in</sub> = 20 dBm, I <sub>DQ</sub> = 150 mA, f = 850 MHz)	ηD	65	70	-	%



C1, C13	1000 pF, ATC "B" Series	L2	7 Turns, AWG #18, 0.09" I.D., Close Wound
C2	2.7 pF, ATC "B" Series	L3	3 Ferrite Beads on 1/2" AWG #16
C3	2.7 pF, ATC "B" Series	R1	680 $\Omega$ , 1/8 Watt Leaded
C4	7.5 pF, ATC "B" Series	Z1	0.075" x 0.790" Microstrip
C5	33 pF, ATC "B" Series	Z2	0.075" x 0.09" Microstrip
C6, C12	47 μF, Ceramic	Z3, Z4	0.075" x 0.25" Microstrip
C7, C8, C9, C10, C11	0.05 μF Chip	<b>Z</b> 5	0.075" x 0.09" Microstrip
L1, L4	VK-200 4 Turn Ferrite Bead	Z6	0.075" x 0.53" Microstrip
		Substrate	e Material: 0.05, Teflon/Glass, $\varepsilon_{\Gamma}$ = 2.55, 2 oz. cu.

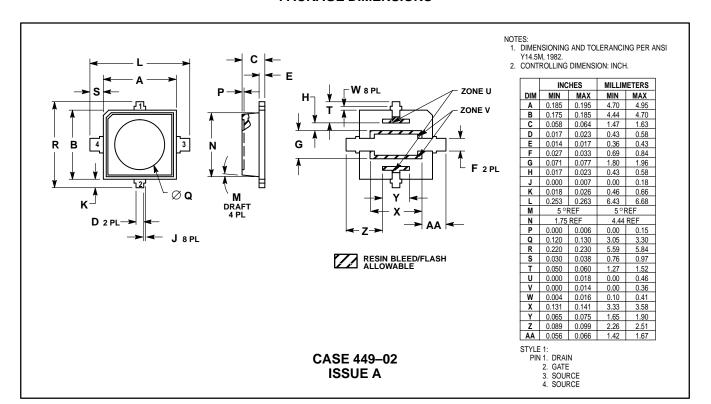
Figure 1. 850 MHz Test Fixture Schematic

Table 1. Large Signal Impedance  $V_{DD}$  = 3.5 V,  $P_{in}$  = 20 dBm,  $I_{DQ}$  = 150 mA

f	Z <sub>in</sub>	Z <sub>OL</sub> *
MHz	Ohms	Ohms
850	5.0 – j6.3	5.5 – j1.2

Z<sub>OL</sub>\* is the conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

#### **PACKAGE DIMENSIONS**



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